Q.P. Code: 18EE2113												R18		
		Reg. No.]	
		SIDDHA DIGITA	ARTH I M.Tec L CON	INSTI h I ye NTRO	ITUTH ar II S L OF	E OF] (AU Semest POW	ENGI UTON ter Ra 'ER E	NEEF IOMC egular LECT	RING DUS) Exam TRON	& TE inati IC Al	CHN ons Ju ND DI	OLO ine 20 RIVE	GY::PUTTUR 019 SYSTEMS	
						(Po	ower E	lectro	nics)					
Time: 3 hoursMax. Marks:64											ax. Marks:60			
1	Ext	plain the ap	plicatio	(on of r	Answe	er all F	Five U UNI ethods	nits 5> T I to sol	<12=60 ve tran) Mar	ks) s in D.	C. sw	itched R-C.	12M
-		P	P				(DR			,	0.01		
2	Exj	Explain the application of numerical methods to solve transients in D.C. switched R-L.												
3	a	Discuss the	he worl	king of	f diode	with	R loa	d with	AC su	pply.				6M
b Discuss the working of diode with R-L load with AC supply.													6M	
							(OR						
4	Wr	ite about th	e mode	eling o	f SCR	and T	RIAC	c in sin	nulatio	n.				12M
							UNI	III 7						
5 a Explain the electrical machine modeling of DC machine.												6M		
	b Give the simulation diagram of basic electric drives.												6M	
							(OR						
6	Explain the State space modeling and simulation of linear systems.													12M
7	a	Explain the	he Sim	ulatior	n of sin	gle pł	nase u	ncontr	olled r	ectifi	ers.			6M
	b	Explain the	he Sim	ulatior	n of sin	gle pł	nase c	ontroll	ed (SC	CR) re	ctifier	s.		6M
							(OR						
8	8 What are the power factor correction schemes and explain with neat figures?													12M
9	Ex	plain in det	ail the	Simul	ation o	of thyr	istor c	hoppe	rs witł	ı volta	age co	mmut	ation schemes.	12M
							(OR						
10	Ex]	plain Pulse-	width	modul	ation r	nethoo	ds for *** E	voltag ND**	e conti *	ol an	d Wav	eform	n control.	12M